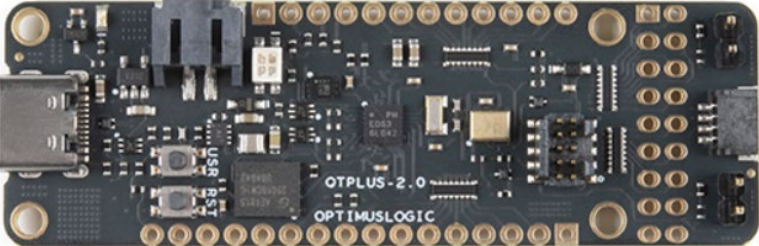


QuickLogic* - EOS* S3 MCU + eFPGA SoC

SensiML Supported Development Kit	Processor	QuickLogic EOS S3 Arm* Cortex*-M4, 32-bit with eFPGA (2400 effective logic cells)
 <p>The image shows a black PCB development board with a USB Type-C port on the left, a micro-USB port on the right, and a header with 20 pins. The board is populated with various components including a microcontroller, memory, and sensors. Text on the board includes 'OTPLUS-2.0' and 'OPTIMUSLOGIC'.</p>	Pre-enabled Sensor Types i	ST Microelectronics* LIS2DH12TR 3DoF accel Vesper* VM3011-U1 microphone
	Additional Available Sensors i	SparkFun* QWIIC* sensor boards
	Available External Sensor Interfaces	UART, I2C (QWIIC), SPI, I2S
	Pre-enabled Connectivity	USB, Serial
	Programming Environment	IDEs: Eclipse* IDE Compilers: Arm Embedded GCC
	Firmware Flashing i	QuickLogic Thing Plus has built-in programming and debugger via microUSB connection to PC, no separate board or debug cable req'd
	SensiML Knowledge Pack Formats	Binary , Library , C Source
	Useful Links	SensiML Getting Started Guide , HW Datasheet , SparkFun Documentation Links
	SparkFun QuickLogic Thing Plus - EOS S3	

* All product and company names are property of their respective holders. Use of them does not imply any affiliation with or endorsement by them.